



KAmoDRPi-Pmod-HAT



Rev. 20200923112522

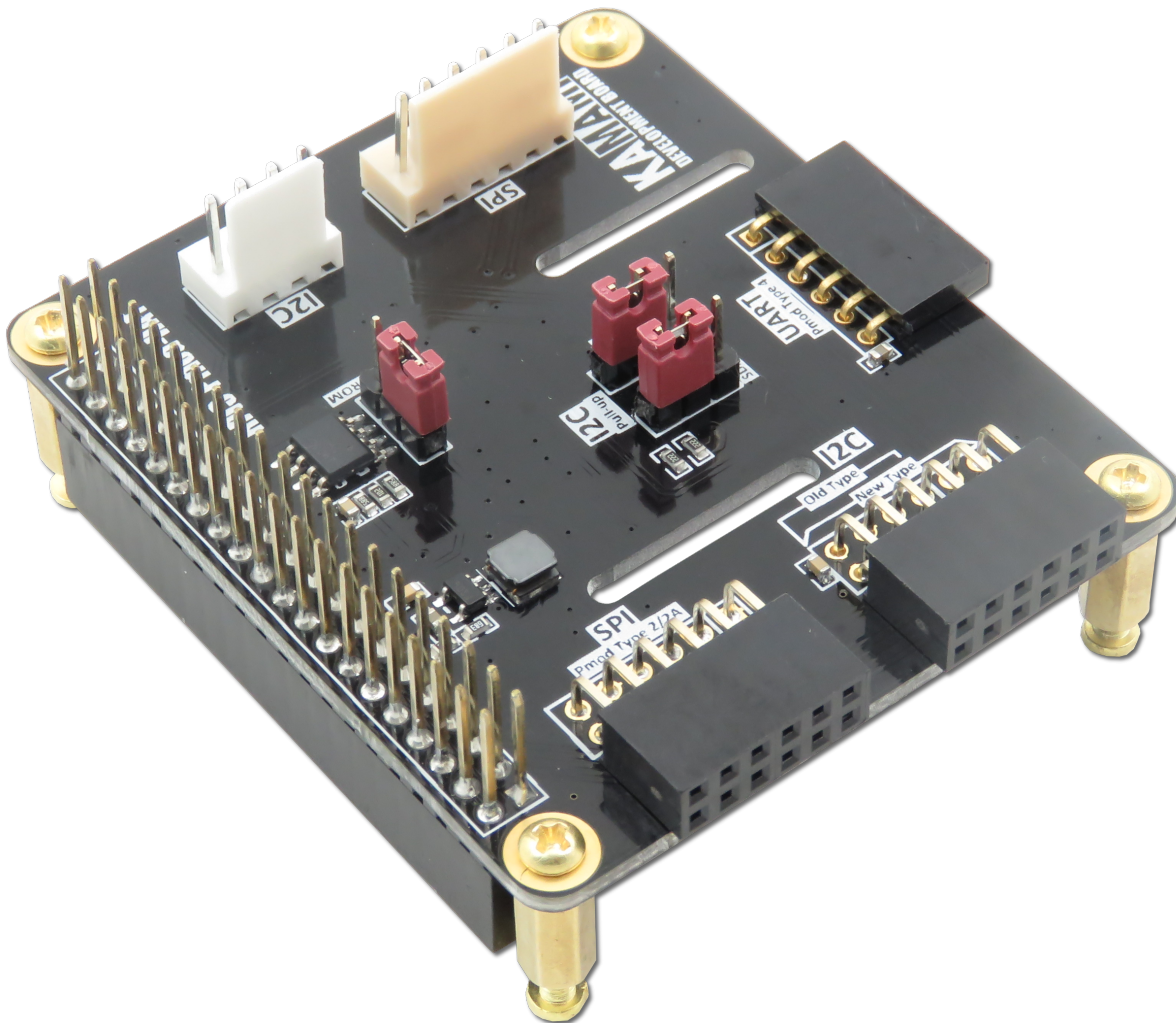
Źródło: <https://wiki.kamamilabs.com/index.php/KAmoDRPi-Pmod-HAT>

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Description

[KAmoDRPi-Pmod-HAT](#) is a HAT for Raspberry Pi 4B/3B+/3B/2B/B+ minicomputers that allows use of the communication interfaces available on the 40-pin minicomputer connector. The overlay provides connectors in the Pmod standard and in the KAMAMI standard for I2C, SPI and UART interfaces.



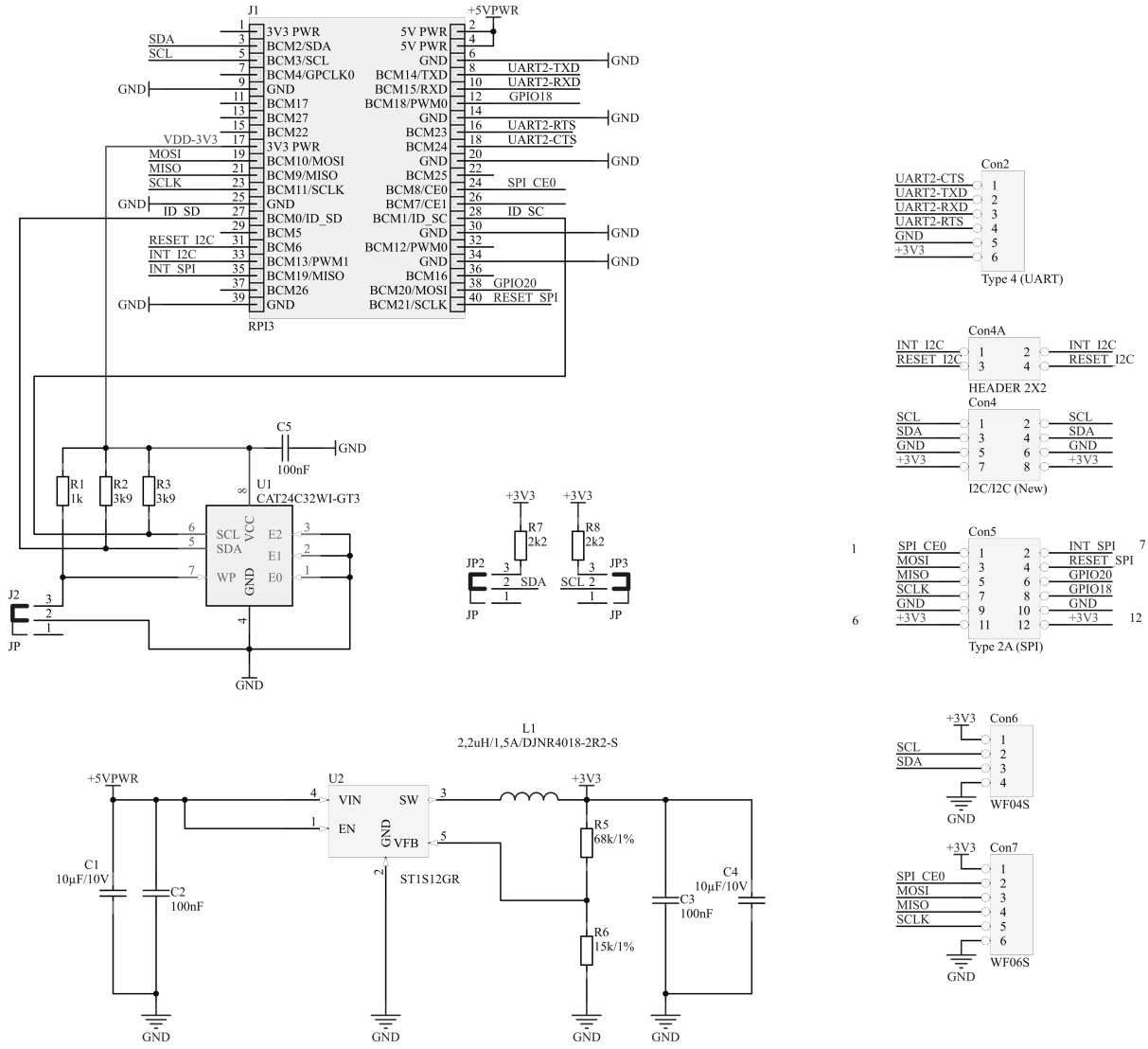
Basic parameters

- Expansion module for Raspberry Pi 4B, 3B+, 3B, 2B, B+ minicomputers
- Embedded ID-EEPROM memory
- Connectors for I2C, SPI and UART interfaces
- Pmod connectors
- KAMAMI connectors
- 40-pin through connector compatible with the Raspberry Pi standard
- Embedded jumpers enabling pull-up on I2C bus lines
- Mounting holes with a diameter of 2.5 mm
- Dimensions: 65 mm x 56 mm x 16 mm

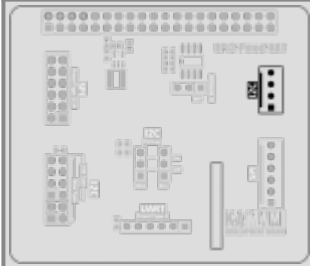
Standard equipment

Code	Description
KAmoDRPi-Pmod-HAT	<ul style="list-style-type: none">• Assembled and launched module• Four spacers with a set of screws for mounting

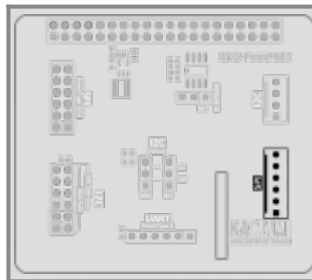
Schematic



Pin description - I2C KAMAMI standard connector

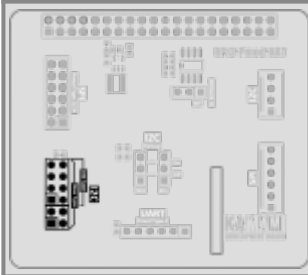
	Pin number	Function
1 (VDD_3,3V)	Power supply (max. 3,3 V)	
2 (SCL)	Clock line of the I2C bus	
3 (SDA)	Data line of the I2C bus	
4 (GND)	Ground	

Opis wyprowadzeń - złącze SPI standardu KAMAMI



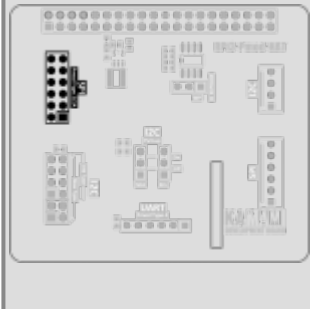
Pin number	Function
1 (VDD_3,3V)	Power supply (max. 3,3 V)
2 (CS)	CS line of the SPI bus
3 (MOSI)	Data line to peripheral chip of the SPI bus
4 (MISO)	Data line from peripheral chip of the SPI bus
5 (CLK)	Clock line of the SPI bus
6 (GND)	Ground

Opis wyprowadzeń - złącze I2C standardu Pmod

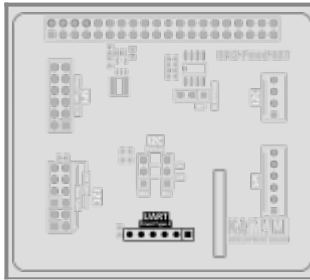


Pin number	Function
1, 2 (SCL)	Clock line of the I2C bus
3, 4 (SDA)	Data line of the I2C bus
5, 6 (GND)	Ground
7, 8 (VDD_3,3V)	Power supply (max. 3,3 V)
9, 10 (INT)	Interrupt line
11, 12 (RESET)	Reset line

Opis wyprowadzeń - złącze SPI standardu Pmod

	Pin number	Function	Pin number	Function
	1 (CS)	CS line of the SPI bus	7 (INT SPI)	Interrupt line
	2 (MOSI)	Data line to peripheral chip of the SPI bus	8 (RESET SPI)	Reset line
	3 (MISO)	Data line from peripheral chip of the SPI bus	9 (GPIO20)	Linia I/O
	4 (CLK)	Clock line of the SPI bus	10 (GPIO18)	Linia I/O
	5 (GND)	Ground	11 (GND)	Ground
	6 (VDD_3,3V)	Power supply (max. 3,3 V)	12 (VDD_3,3V)	Power supply (max. 3,3 V)

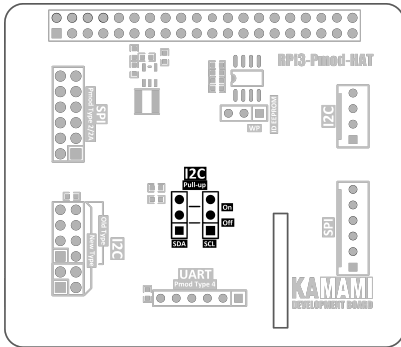
Opis wyprowadzeń - złącze UART standardu Pmod



Pin number	Function
1 (CTS)	CTS line of the UART bus
2 (TXD)	TX data line of the UART bus
3 (RXD)	RX data line of the UART bus
4 (RTS)	RTS line of the UART bus
5 (GND)	Ground
6 (VDD_3,3V)	Power supply (max. 3,3 V)

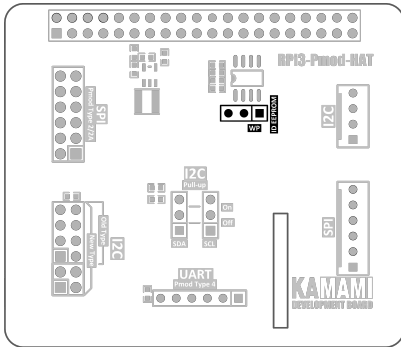
I2C bus lines

The KAmoRPi-Pmod-HAT module is equipped with jumpers that allow the connection of pull-up resistors to the VCC to the I2C bus line. Jumpers allow the independent activation of pull up for SDA and SCL lines.

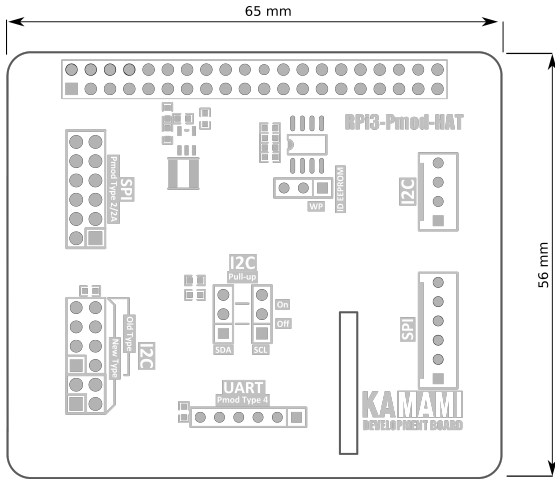


Write protection of the ID EEPROM memory

The KAmoRPi-Pmod-HAT module is equipped with a jumper that allows write block to EEPROM memory. Write Protect protects against unwanted attempts to write data to memory.



Dimensions





Zastrzegamy prawo do wprowadzania zmian bez uprzedzenia.

Oferowane przez nas płytki drukowane mogą się różnić od prezentowanej w dokumentacji, przy czym zmianom nie ulegają jej właściwości użytkowe.

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